

V_{DC}	650 V
Q_C	120 nC
I_F	50 A
$T_{j,max}$	175 °C

650V/50A SiC Schottky Diode Bare Die

Amp+™ Features

- Unipolar rectifier with surge current
- Zero reverse recovery current
- Fast, temperature-independent switching
- Reduced temperature dependence of V_F

Chip Outline



Part #	Die Size	Anode	Cathode
GP3D050A065X	3.50 x 3.50 mm	Al	Ni/Ag

Maximum Ratings, at $T_j=25\text{ °C}$, unless otherwise specified

Characteristics	Symbol	Conditions	Values	Unit
Continuous forward current	I_F	$T_C=25\text{ °C}$, $T_j=175\text{ °C}$	135*	A
		$T_C=125\text{ °C}$, $T_j=175\text{ °C}$	70*	
		$T_C=150\text{ °C}$, $T_j=175\text{ °C}$	45*	
Surge non-repetitive forward current sine halfwave	I_{FSM}	$T_C=25\text{ °C}$, $t_p=8.3\text{ ms}$	300*	A
		$T_C=110\text{ °C}$, $t_p=8.3\text{ ms}$	275*	
Non-repetitive peak forward current	$I_{F,max}$	$T_C=25\text{ °C}$, $t_p=10\text{ }\mu\text{s}$	2000***	A
Repetitive peak reverse voltage	V_{RRM}	$T_j=25\text{ °C}$	650	V
Operating junction & storage temperature	T_j , $T_{storage}$	Continuous	-55...175	°C

Values have been verified on TO-247 packaged devices

*Assumes R_{thJC} thermal resistance of 0.35°C/W with recommended wire bond

** Verified by 100% wafer test

*** Limited by testing equipment

Electrical Characteristics, at $T_j=25\text{ }^\circ\text{C}$, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
DC blocking voltage	V_{DC}	$T_j=25\text{ }^\circ\text{C}$	650**	-	-	V
Breakdown voltage	V_{BR}	$I_R=1.65\text{mA}$, $T_j=25\text{ }^\circ\text{C}$	715**			
Diode forward voltage	V_F	$I_F=50\text{A}$, $T_j=25\text{ }^\circ\text{C}$	-	1.45	1.60	V
		$I_F=50\text{A}$, $T_j=125\text{ }^\circ\text{C}$	-	1.61	-	
		$I_F=50\text{A}$, $T_j=175\text{ }^\circ\text{C}$	-	1.77	2.20	
Reverse current	I_R	$V_R=650\text{V}$, $T_j=25\text{ }^\circ\text{C}$	-	6**	125**	μA
		$V_R=715\text{V}$, $T_j=25\text{ }^\circ\text{C}$	-	22**	-	
		$V_R=650\text{V}$, $T_j=125\text{ }^\circ\text{C}$	-	41	-	
		$V_R=650\text{V}$, $T_j=175\text{ }^\circ\text{C}$	-	147	500	
Total capacitive charge	Q_C	$V_R=400\text{V}$, $T_j=25\text{ }^\circ\text{C}$	-	120	-	nC
Total capacitance	C	$V_R=1\text{V}$, $f=1\text{ MHz}$	-	1946	-	pF
		$V_R=200\text{V}$, $f=1\text{ MHz}$	-	228	-	
		$V_R=400\text{V}$, $f=1\text{ MHz}$	-	189	-	

Values have been verified on TO-247 packaged devices

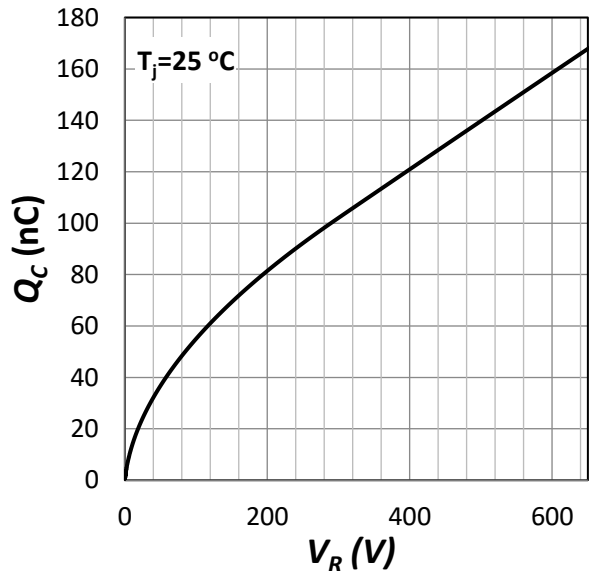
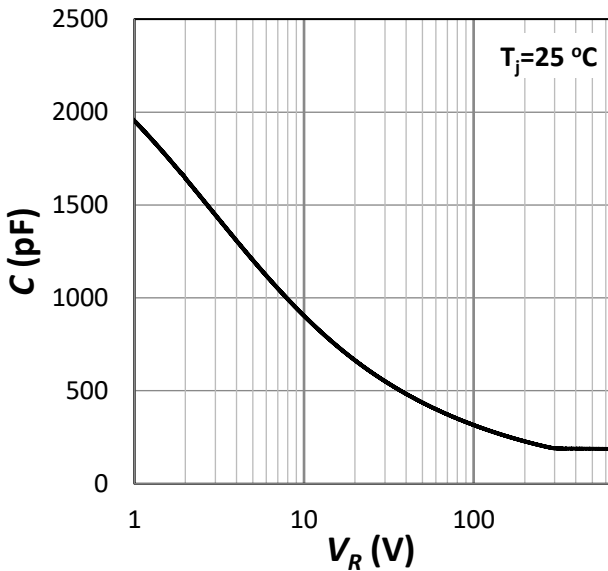
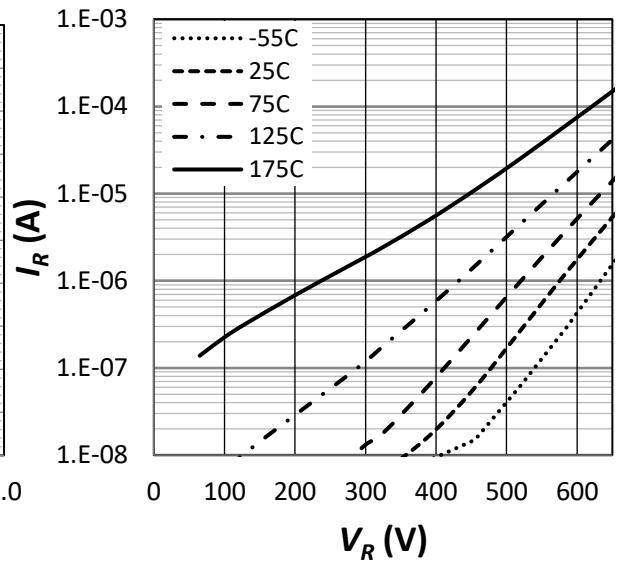
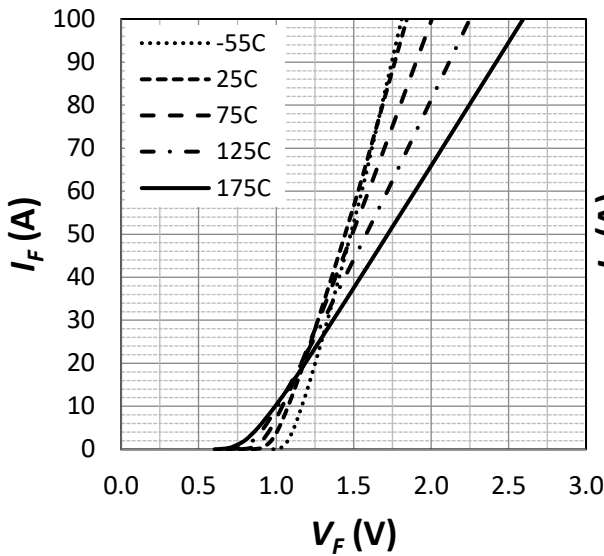
*Assumes R_{thJC} thermal resistance of $0.35\text{ }^\circ\text{C/W}$ with recommended wire bond

** Verified by 100% wafer test

Mechanical Parameters

Parameter	Typ.	Unit
Die Size	3.50 x 3.50	mm
Anode Pad Opening	3.00 x 3.00	mm
Recommended Wire Bond (TO-247)	15 mil x 4	mil
Die Thickness	175 ± 25	μm
Wafer Size	150	mm
Anode Metalization (Al)	4	μm
Cathode Metalization (Ni/Ag)		μm
Frontside Passivation	Polyimide on Silicon Nitride	

Typical Performance in packaged device



Notes**RoHS Compliance**

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented March, 2013. RoHS Declarations for this product can be obtained from the Product Documentation sections of www.SemiQ.com.

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