

QSiC™ 1200V SiC Half-Bridge Module

V_{DS}	1200 V
$R_{DS,on}$	2.0 mΩ
$I_D (T_C=25°C)$	724 A
$T_{J,max}$	175°C

Features

- 62mm x 152mm industry standard footprint
- High speed switching SiC MOSFETs
- All parts tested to greater than 1,350V
- Kelvin reference for stable operation
- AlN Isolated baseplate

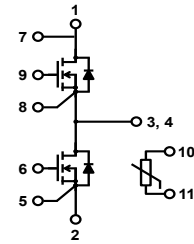
Benefits

- Low switching losses
- Low junction to case thermal resistance
- Very rugged and easy mounting
- Direct mounting to heatsink (isolated package)
- Lower Q_{RR} at high temperature

Applications

- Motor drives
- EV applications
- Smart-grid
- Uninterruptible power supply (UPS)

Package



Part #	Package	Marking
GCMX2P0B120S4B1-N	S4	GCMX2P0B120S4B1-N



Absolute Maximum Ratings, at $T_J=25°C$, unless otherwise specified

Characteristics	Symbol	Conditions	Values	Unit
Drain-Source Voltage	V_{rated}	$V_{GS}=0V, I_{DS}=7\mu A$	1200	V
Continuous Drain Current	I_{DS}	$T_C=25°C, V_{GS}=18V, T_J=175°C$	724	A
		$T_C=65°C, V_{GS}=18V, T_J=175°C$	624	
Body Diode Drain Current	I_{SD}	$T_C=25°C, V_{GS}=-4.5V, T_J=175°C$	481	
Pulsed Drain Current	$I_{DS,pulse}$	$T_C=25°C, V_{GS}=18V$	1400	
Gate Source Voltage	V_{GSmax}		-8/22	V
	V_{GSop}	Recommended operational	-4.5/18	
Power Dissipation	P_{tot}	$T_C=25°C, T_J=175°C$	1847	W
Junction Temperature	T_J	Continuous	-40...175	°C
Case & Storage Temperature	$T_C, T_{storage}$	Continuous	-40...150	°C

Static Electrical Characteristics, at T_J=25°C, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _{DS} =7uA	1200	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =1200V, V _{GS} =0V	-	1.0	7.0	uA
		V _{DS} =1200V, V _{GS} =0V, T _J =150°C	-	5.9	70	
Gate-Source Leakage Current	I _{GSS+}	V _{GS} =22V, V _{DS} =0V	-	70	2000	nA
	I _{GSS-}	V _{GS} =-8V, V _{DS} =0V	-	-70	-2000	
Gate Threshold Voltage	V _{GS(th)}	V _{GS} =V _{DS} , I _{DS} =140mA	1.8	2.9	4	V
		V _{GS} =V _{DS} , I _{DS} =140mA, T _J =150°C	-	2.1	-	
Drain-Source On-Resistance	R _{DS(on)} *	V _{GS} =18V, I _{DS} =350A	-	2.0	3.2	mΩ
		V _{GS} =18V, I _{DS} =350A, T _J =150°C	-	3.0	-	
Transconductance	g _{fs}	V _{DS} =20V, I _{DS} =350A	-	136	-	S
		V _{DS} =20V, I _{DS} =350A, T _J =150°C	-	140	-	
Internal Gate Resistance	R _{G(int)}	f=1MHz, V _{AC} =25mV, D-S Short, including internal 0.51 ohm series gate resistor**	-	1.0	-	Ω

*R_{DS(on)} measured at kelvin and sense pins (pin #7-8 and pin #8-5), typical 2mΩ chip only

**Internal series gate resistor limits maximum switching frequency defined by Figure 31

AC Electrical Characteristics, at T_J=25°C, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Input Capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =800V, f=100kHz, V _{AC} =25mV	-	51.9	-	nF
Output Capacitance	C _{OSS}		-	1.67	-	
Reverse Transfer Capacitance	C _{RSS}		-	0.13	-	
Coss Stored Energy	E _{OSS} ***		-	669	-	
Turn-On Switching Energy	E _{ON}	T _J =25°C	-	4.6	-	mJ
		T _J =125°C	-	5.3	-	
		T _J =150°C	-	5.7	-	
Turn-Off Switching Energy	E _{OFF}	T _J =25°C	-	6.1	-	
		T _J =125°C	-	6.6	-	
		T _J =150°C	-	6.8	-	
Turn-On Delay Time	t _{D(on)}	V _{DD} =600V, I _{DS} =350A, R _{G(ext)} =1Ω, V _{GS} =-4.5/+18V, L=9μH,	-	72	-	ns
Rise Time	t _R		-	39	-	
Turn-Off Delay Time	t _{D(off)}		-	173	-	
Fall Time	t _F		-	58	-	
Total Gate Charge	Q _G		-	1806	-	
Gate to Source Charge	Q _{GS}	V _{DD} =800V, I _{DS} =350A, V _{GS} =-4.5/+18V	-	571	-	nC
Gate to Drain Charge	Q _{GD}	-	248	-		

***E_{OSS} is calculated from C_{OSS} curve

Freewheeling Diode Characteristics, at $T_j=25^\circ\text{C}$, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Diode Forward Voltage	V_{SD}	$V_{GS}=-5\text{V}, I_{SD}=350\text{A}$	-	4.2	1.9	V
		$V_{GS}=-5\text{V}, I_{SD}=350\text{A}, T_j=150^\circ\text{C}$	-	3.7	-	
Reverse Recovery Time	t_{RR}	$T_j=25^\circ\text{C}$ $I_S=350\text{A},$ $V_R=600\text{V},$ $V_{GS}=-4.5\text{V},$ $di/dt=9.6\text{A/ns}$	-	11	-	ns
Reverse Recovery Charge	Q_{RR}		-	0.2	-	μC
Peak Reverse Recovery Current	I_{RRM}		-	27	-	A
Reverse Recovery Energy	E_{RR}	$T_j=25^\circ\text{C}$	-	0.05	-	mJ
		$T_j=125^\circ\text{C}$	-	0.78	-	
		$T_j=150^\circ\text{C}$ $R_{G(\text{ext})}=1\Omega$	-	1.12	-	

Thermal and Package Characteristics, at $T_j=25^\circ\text{C}$, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Thermal resistance, junction-case	$R_{thJC, MOSFET}$		-	0.070	0.081	$^\circ\text{C/W}$
Thermal resistance, junction-heatsink	$R_{thJH, MOSFET}$	Thermal grease, Thickness=75 μm , $k = 4.0 \text{ W/mK}$	-	0.093	-	$^\circ\text{C/W}$
Material of module baseplate	-		-	Cu	-	-
Creepage distance	d_{Creep}	terminal to heatsink	-	14.5	-	mm
	d_{Creep}	terminal to terminal	-	13.0	-	mm
Clearance	d_{Clear}	terminal to heatsink	-	12.5	-	mm
	d_{Clear}	terminal to terminal	-	10.0	-	mm
Comparative tracking index	CTI		250	-	400	-
Mounting torque	M_d	M5-0.8 screws	3	-	6	N-m
Terminal connection torque	M_{dt}	M6-1.0 screws	3	-	6	N-m
Package weight	W_t		-	382	-	g
Isolation voltage	V_{ISOL}	$I_{ISOL} < 1\text{mA}, 50/60 \text{ Hz}, 1 \text{ min}$	3400	-	-	V

NTC Characteristics, at $T_j=25^\circ\text{C}$, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Rated resistance	R_{NTC}	$T_{NTC} = 25^\circ\text{C}$	-	5.0	-	k Ω
Resistance tolerance	$\Delta R/R$		-5	-	5	%
Beta Value ($T_2 = 50^\circ\text{C}$)	$\beta_{25/50}$	$R_2 = R_{25} \cdot \exp [B_{25/50} (1/T_2 - 1/(298,15 \text{ K}))]$	-	3375	-	k
Power dissipation	P_{MAX}	$T_{NTC} = 25^\circ\text{C}$	-	-	50	mW

Typical Performance

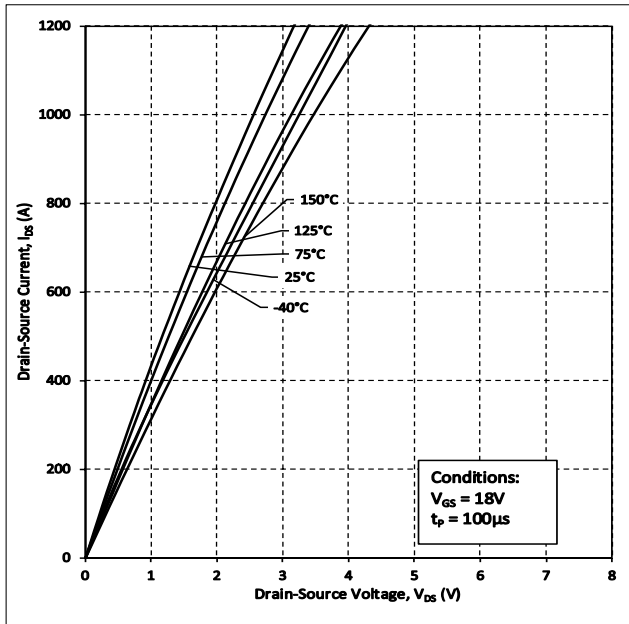


Figure 1. Output Characteristics for Various Temperatures

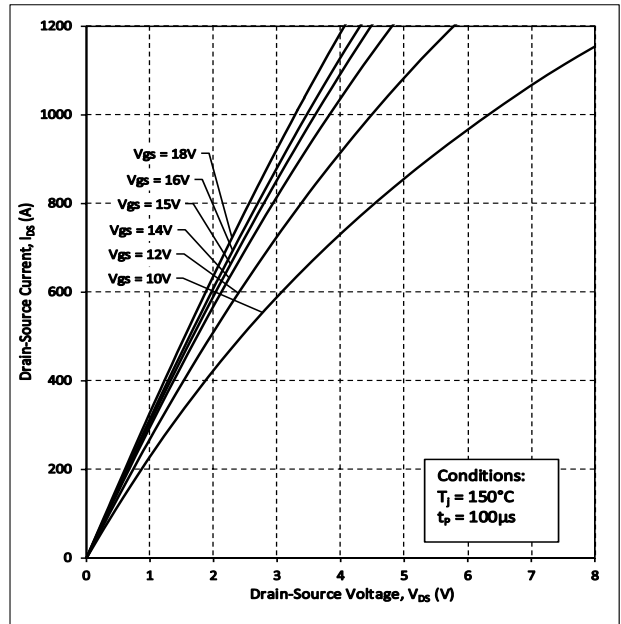


Figure 2. Output Characteristics $T_J = 150^\circ C$

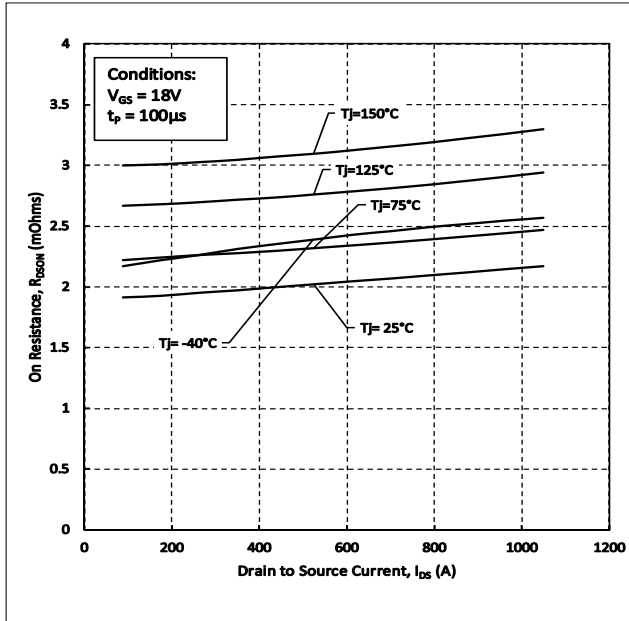


Figure 3. On-Resistance vs. Drain Current For Various Temperatures

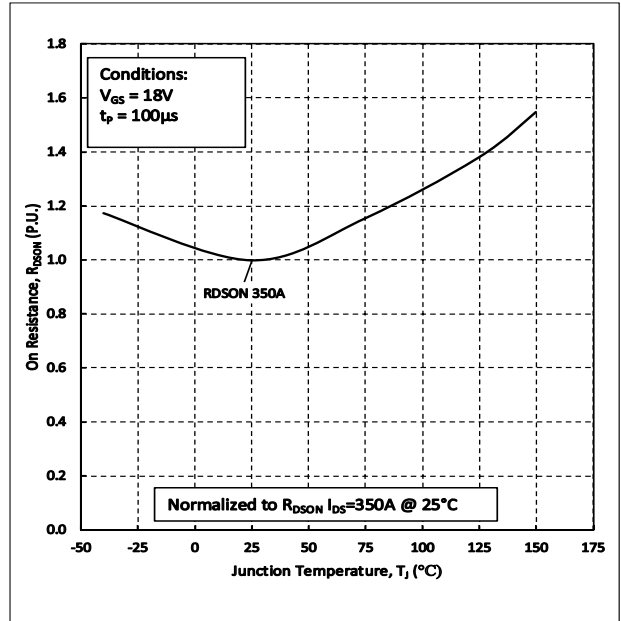


Figure 4. Normalized On-Resistance vs. Temperature

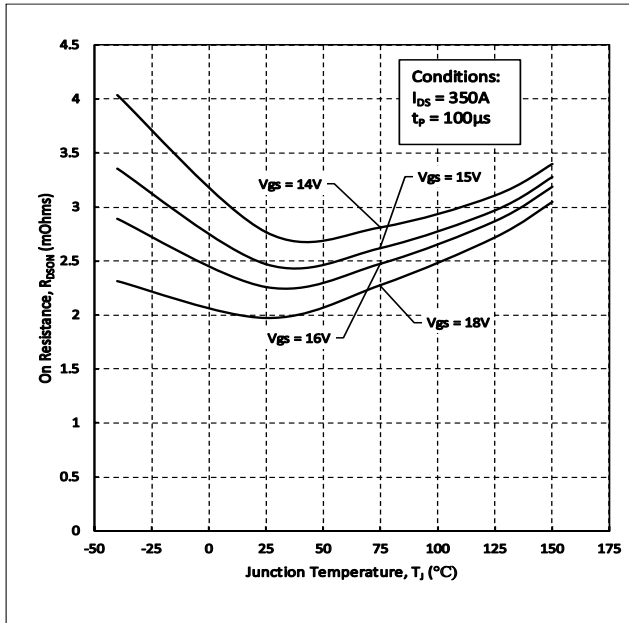


Figure 5. On-Resistance vs. Temperature For Various Gate Voltages

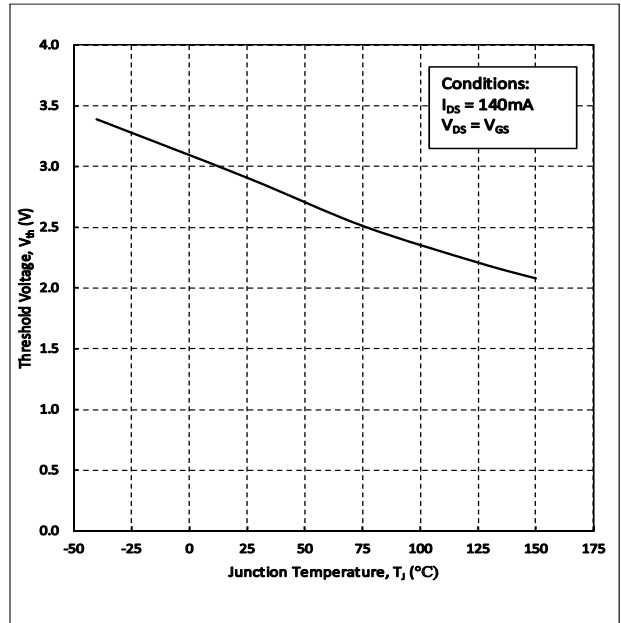


Figure 6. Threshold Voltage vs. Temperature

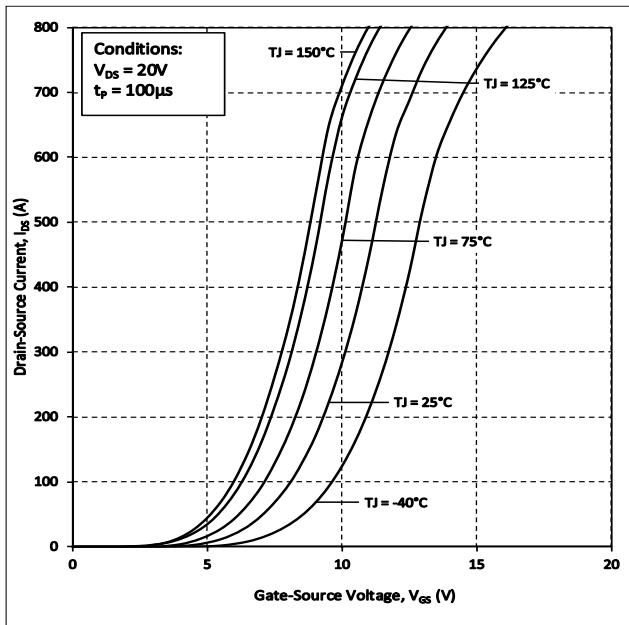


Figure 7. Transfer Characteristic for Various Junction Temperatures

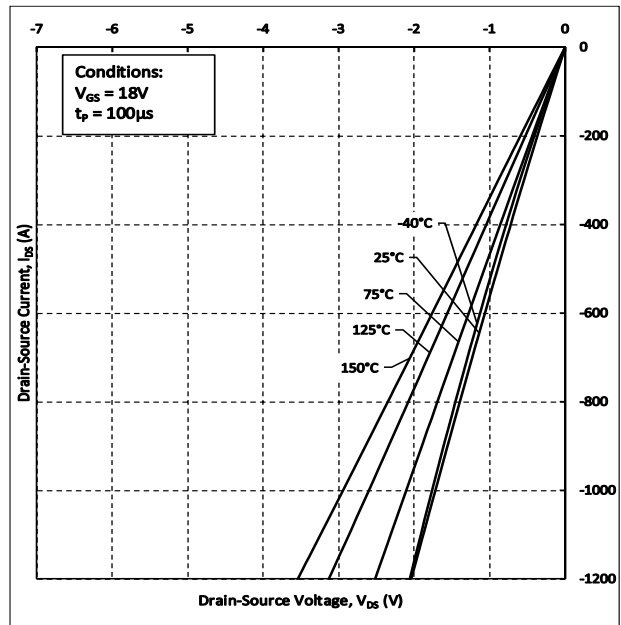


Figure 8. 3rd Quadrant Characteristics at $V_{GS} = 18V$

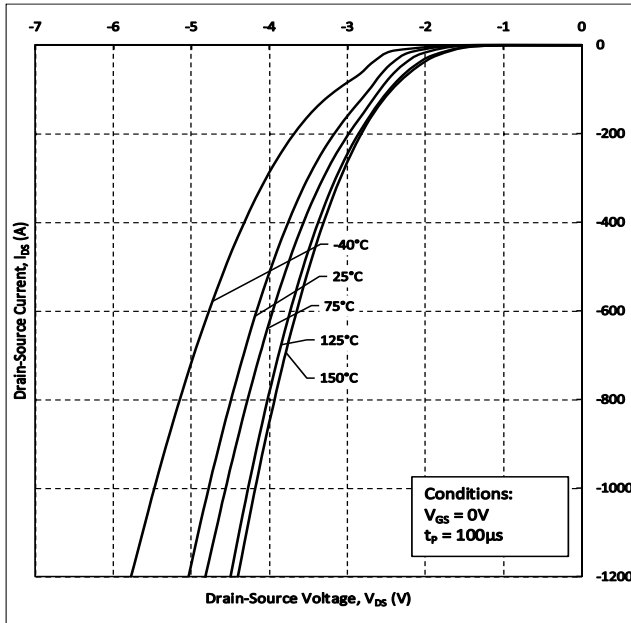


Figure 9. Body Diode Characteristics at $V_{GS} = 0V$

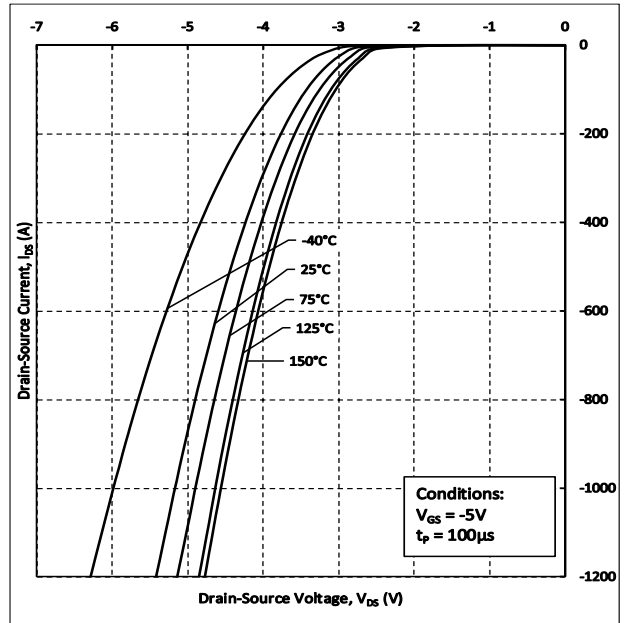


Figure 10. Body Diode Characteristics at $V_{GS} = -5V$

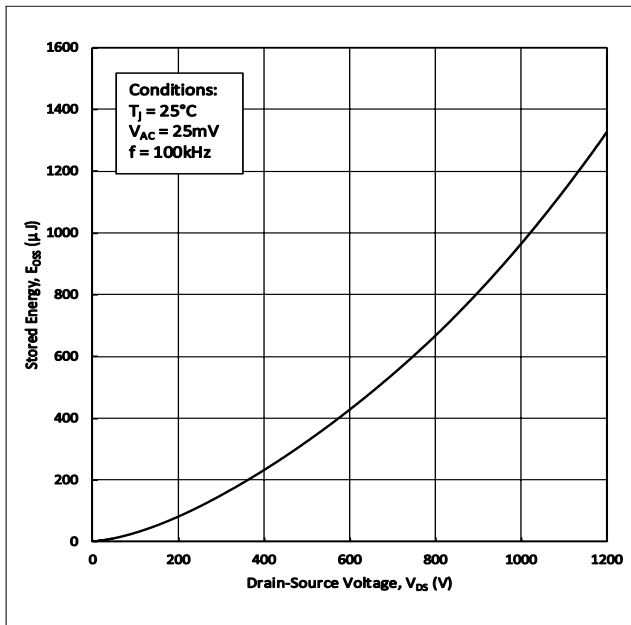


Figure 11. Output Capacitor Stored Energy

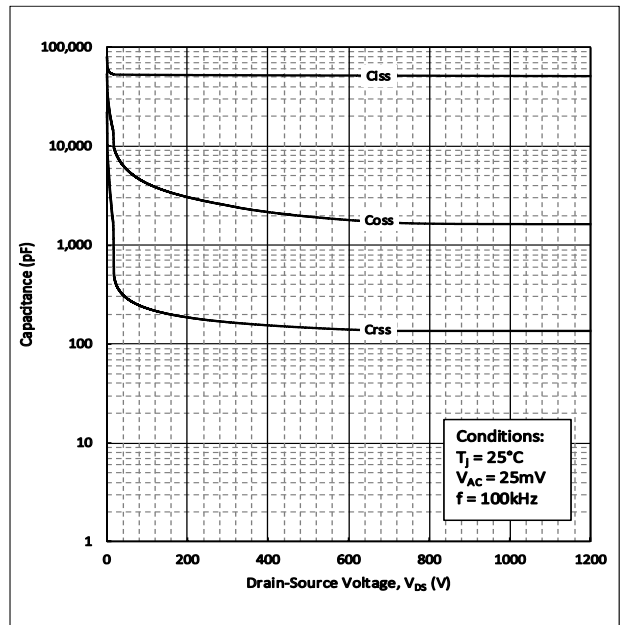


Figure 12. Capacitance vs. Drain-Source Voltage

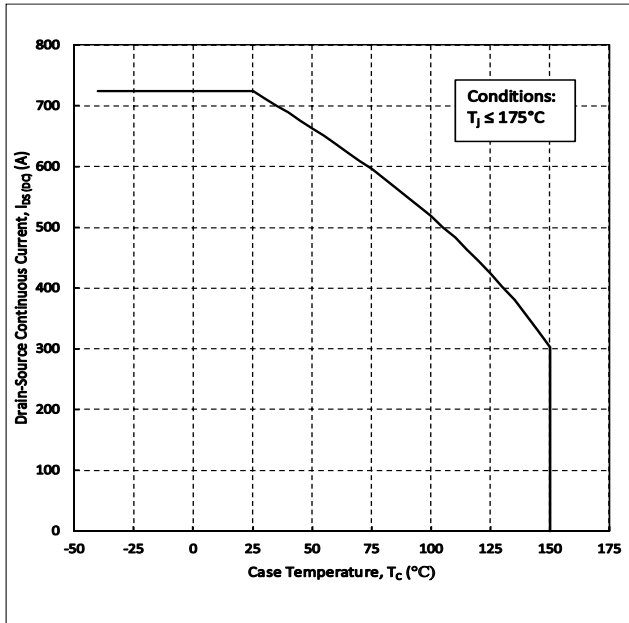


Figure 13. Continuous Drain Current Derating vs. Case Temperature

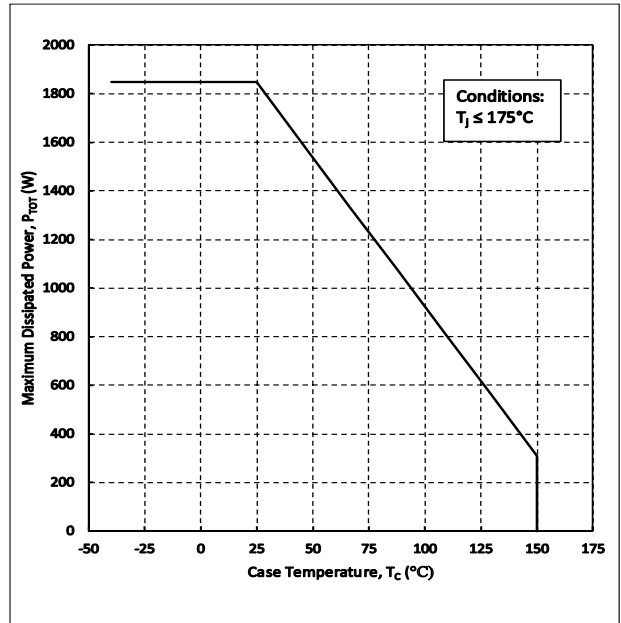


Figure 14. Maximum Power Dissipation Derating vs. Case Temperature

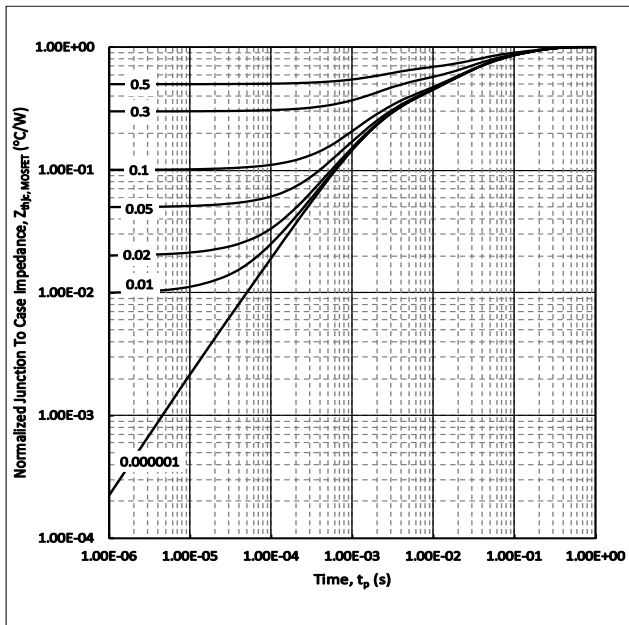


Figure 15. Transient Thermal impedance (Junction to Case)

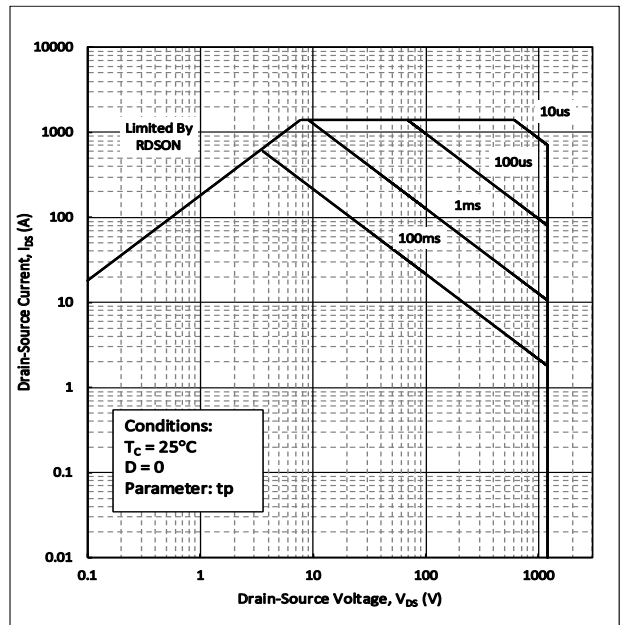


Figure 16. Safe Operating Area

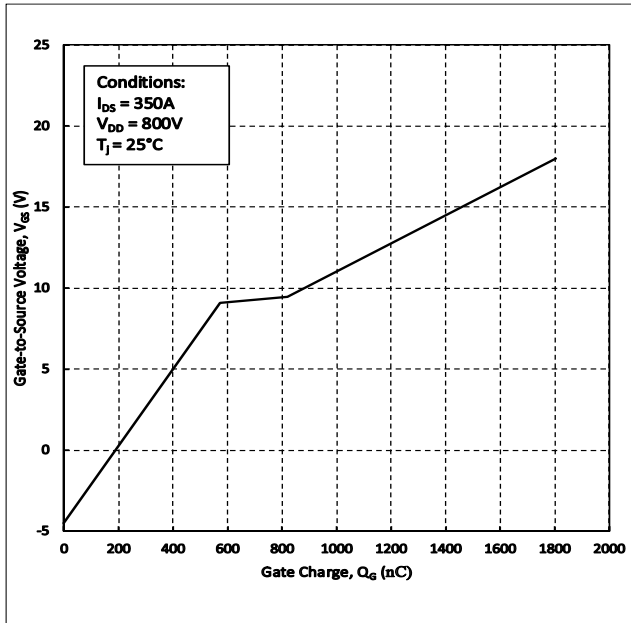


Figure 17. Gate Charge Characteristics

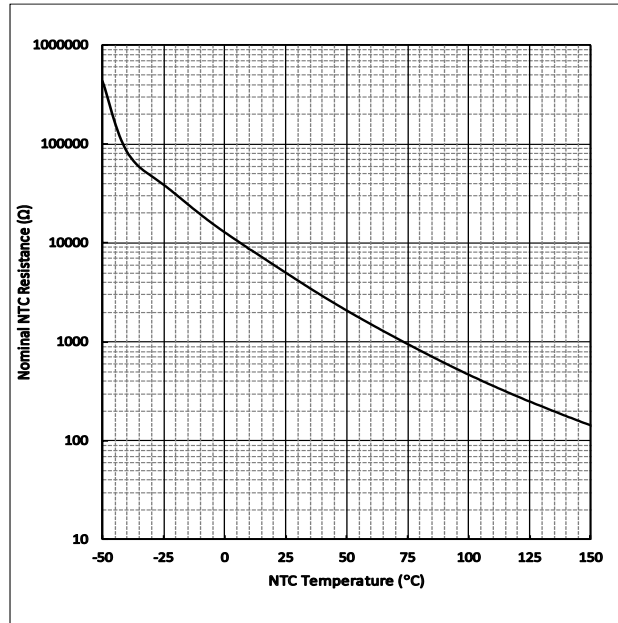


Figure 18. Nominal NTC Resistance vs. Temperature

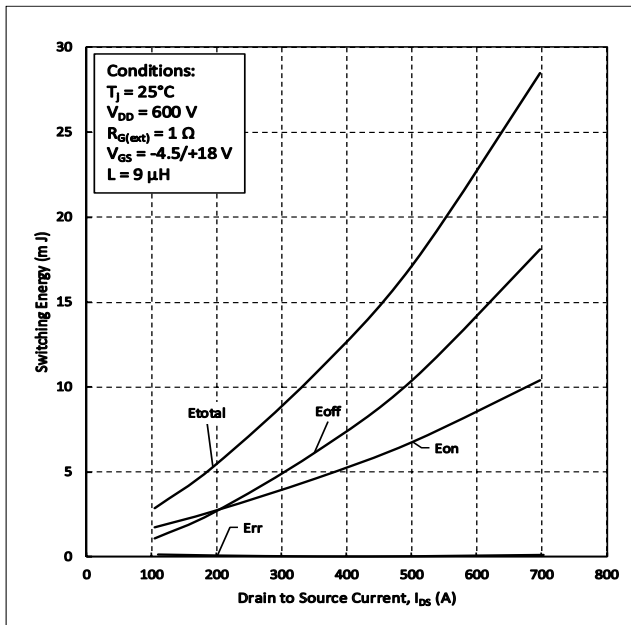


Figure 19. Clamped Inductive Switching Energy vs. Drain Current (600V)

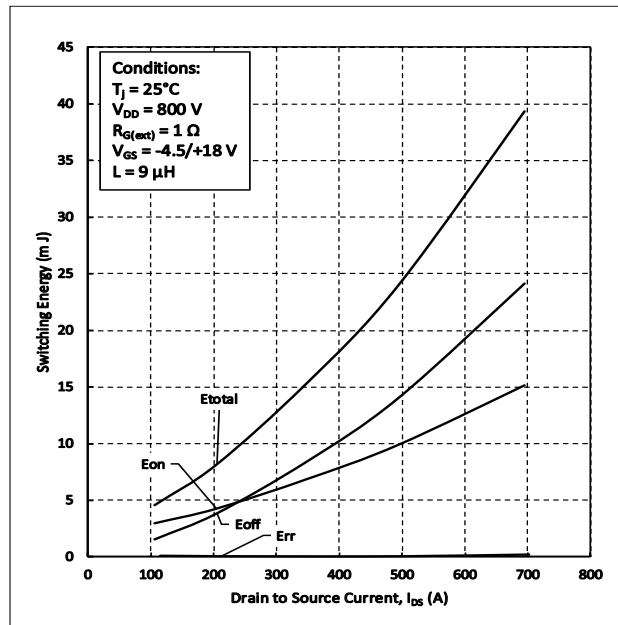


Figure 20. Clamped Inductive Switching Energy vs. Drain Current (800V)

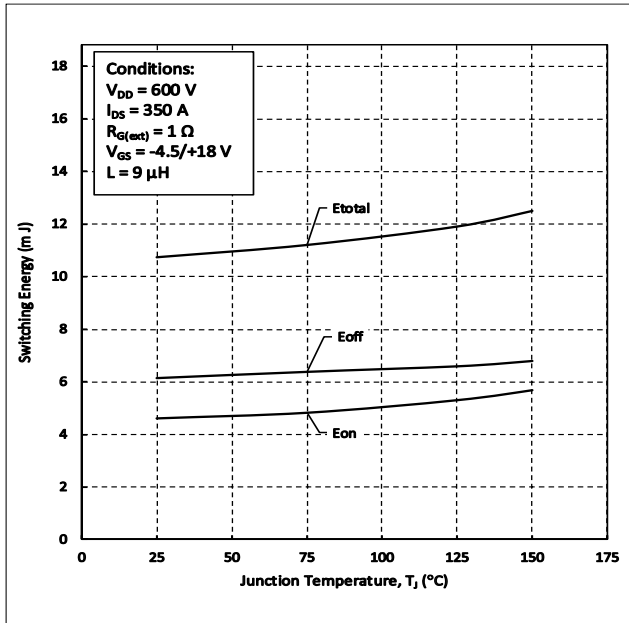


Figure 21. Clamped Inductive Switching Energy vs. Temperature

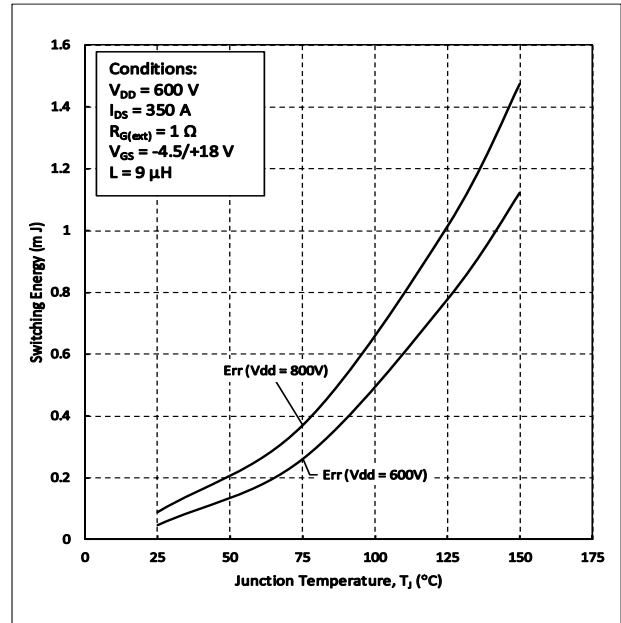


Figure 22. Reverse Recovery Energy vs. Temperature

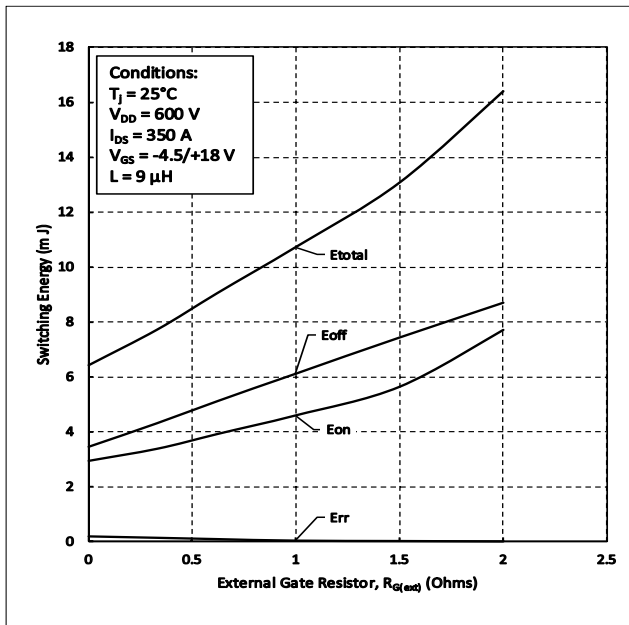


Figure 23. Clamped Inductive Switching Energy vs. $R_{G(ext)}$

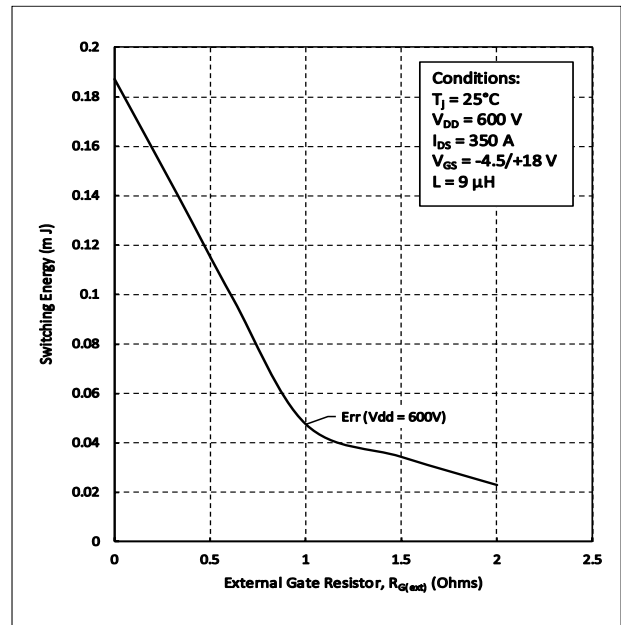


Figure 24. Reverse Recovery Energy vs. $R_{G(ext)}$

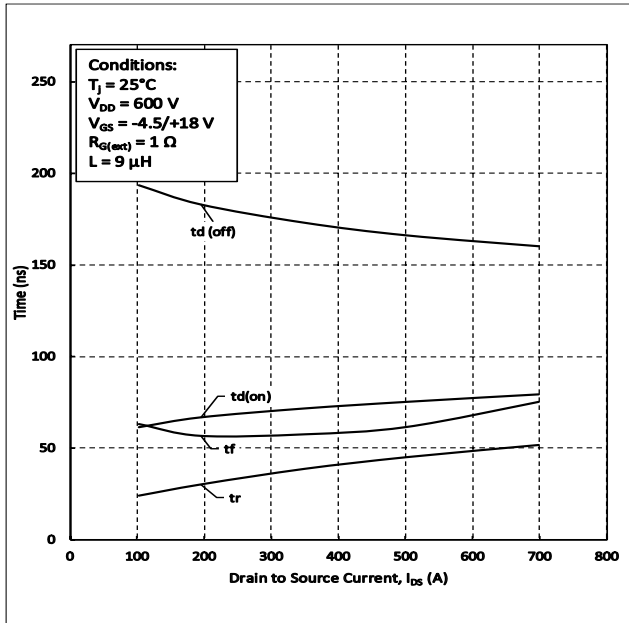


Figure 25. Switching Times vs. Drain Current

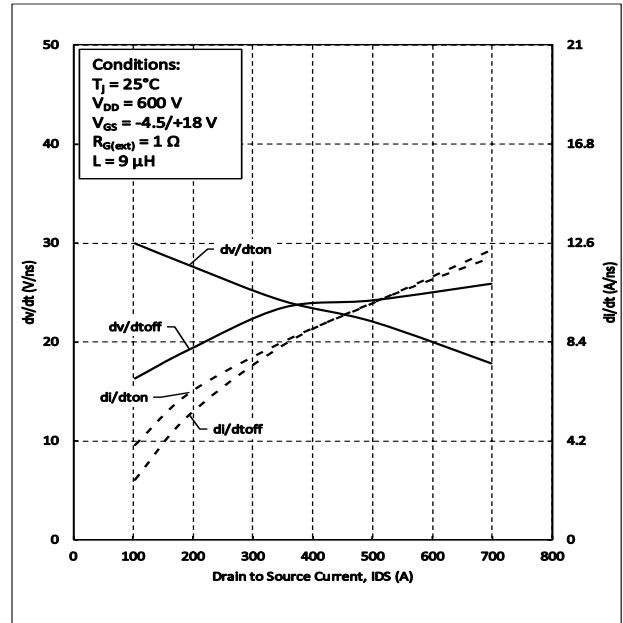


Figure 26. dv/dt and di/dt vs. Source Current

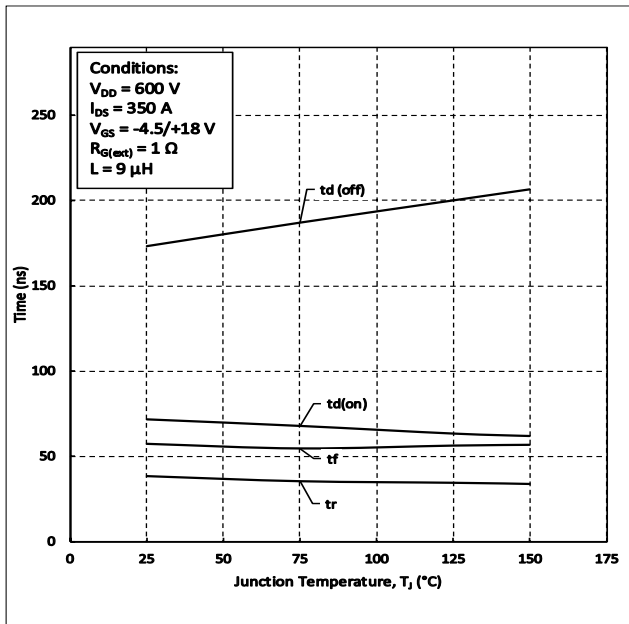


Figure 27. Switching Times vs. Temperature

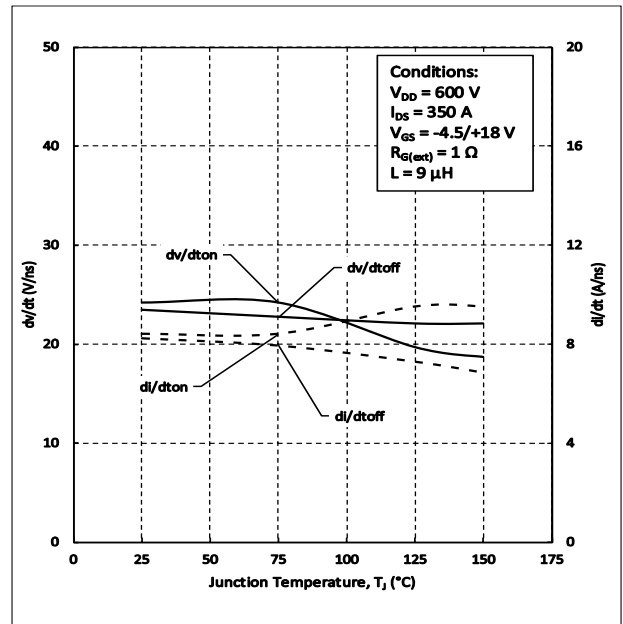


Figure 28. dv/dt and di/dt vs. Temperature

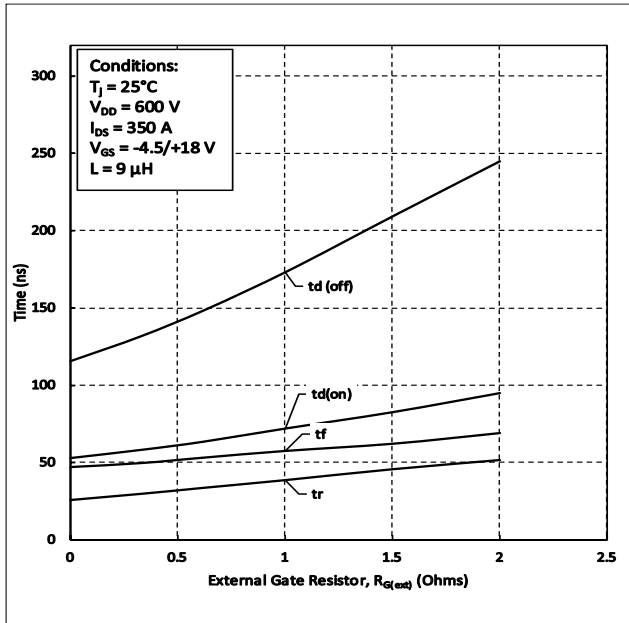


Figure 29. Switching Times vs. $R_{G(ext)}$

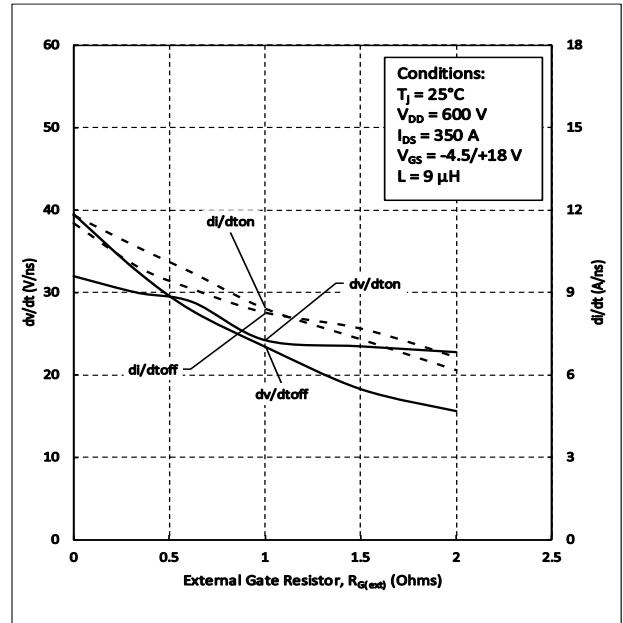


Figure 30. dv/dt and di/dt vs. $R_{G(ext)}$

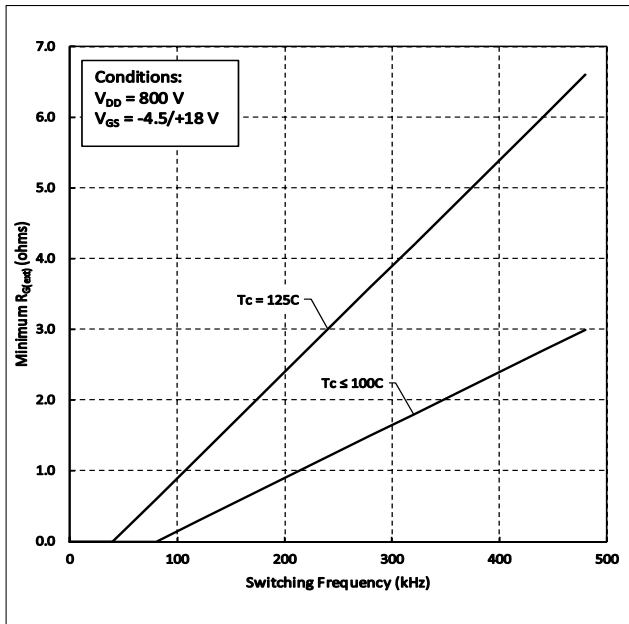


Figure 31. Frequency vs. Minimum $R_{G(ext)}$

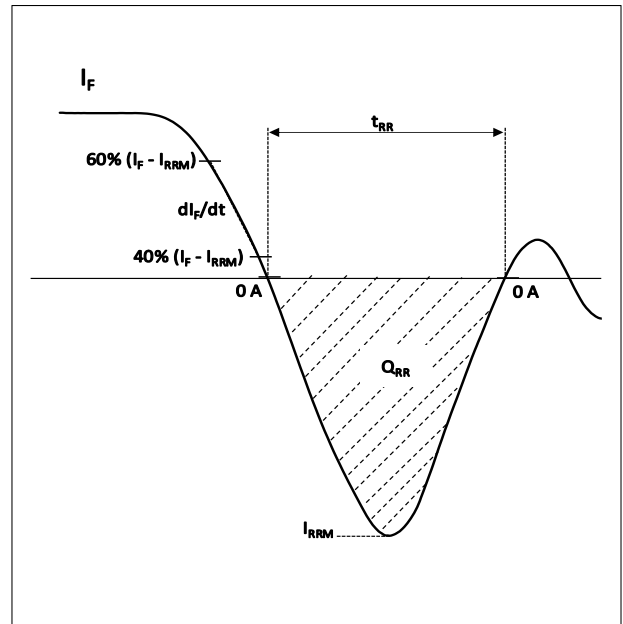


Figure 32. Reverse Recovery Definitions

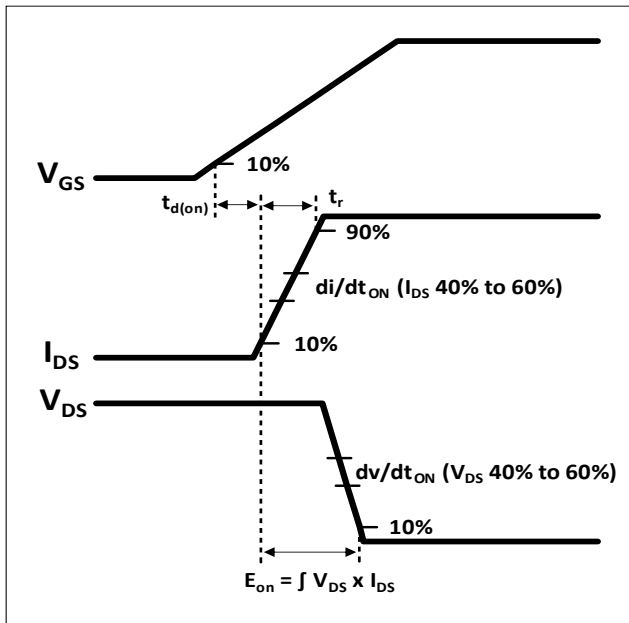


Figure 33. Turn-on Transient Definitions

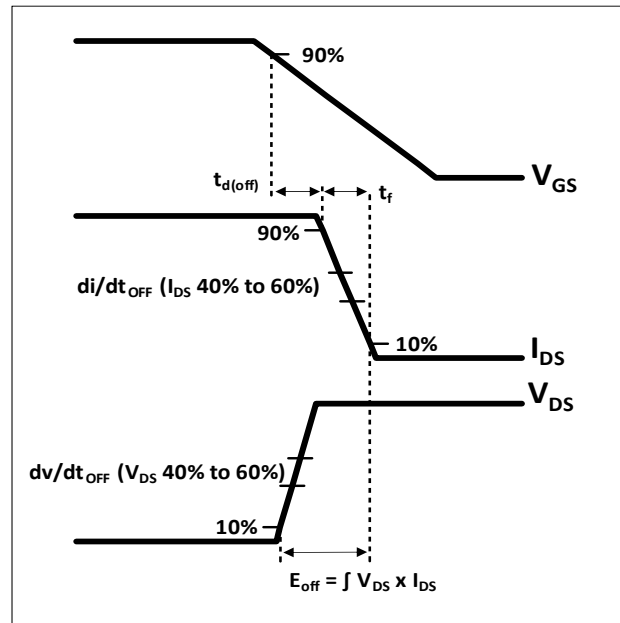
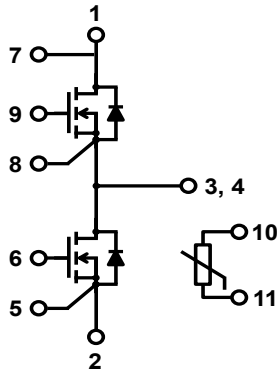


Figure 34. Turn-off Transient Definitions

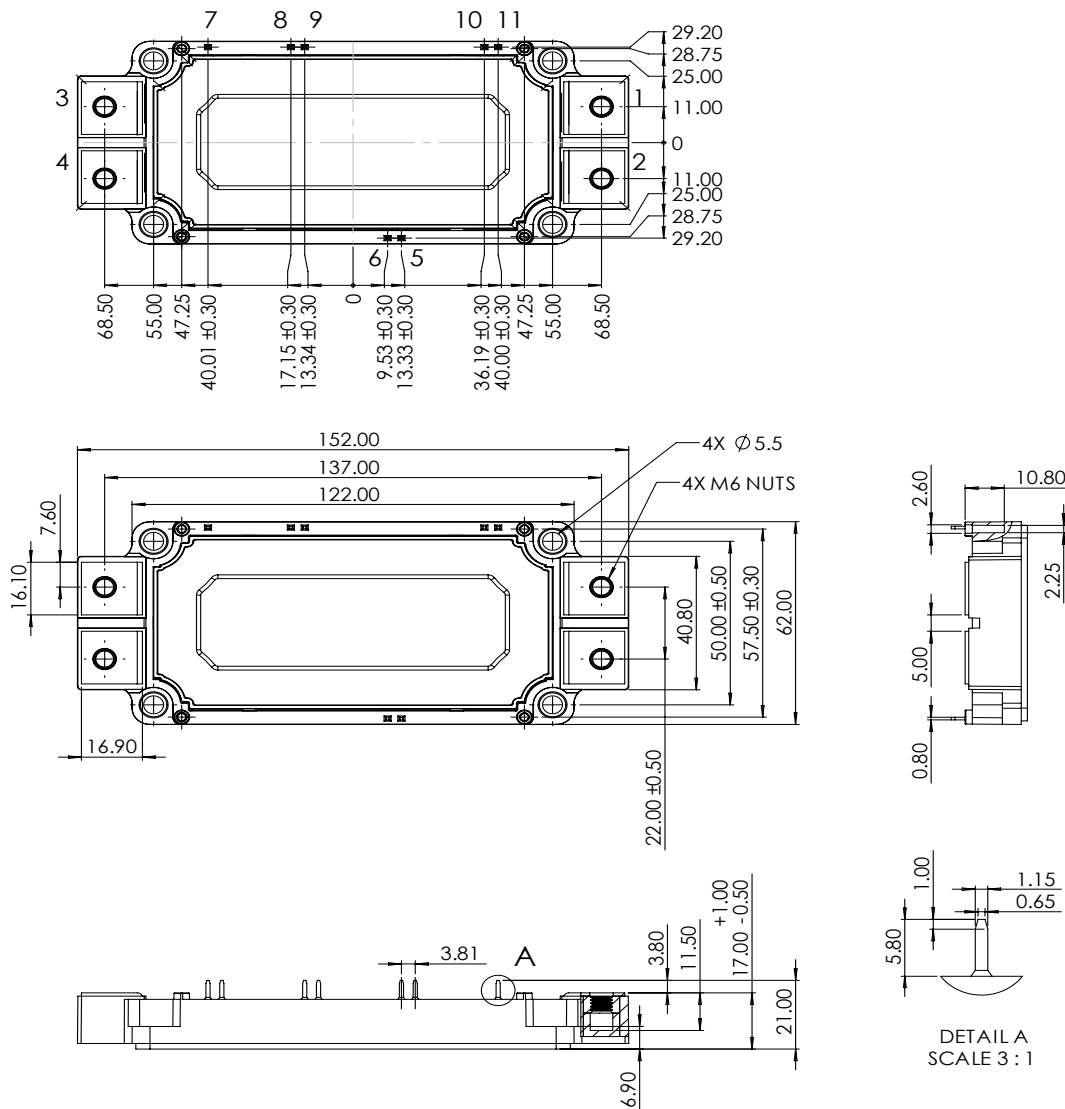
QSiC™ 1200V SiC Half-Bridge Module

GCMX2P0B120S4B1-N

Pinout and Circuit Diagram



Package Dimensions (mm)



Revision History		
Date	Revision	Notes
5/20/2026	1.0	Initial release

Notes

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2015/863 (RoHS3), as implemented July, 2019. RoHS Declarations for this product can be obtained from the Product Documentation sections of www.SemiQ.com.

REACH Compliance

REACH substances of high concern (SVHC) information is available for this product. Since the European Chemicals Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact our office at SemiQ Headquarters in Lake Forest, California to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request. SemiQ, Inc., reserves the right to make changes to the product specifications and data in this document without notice. SemiQ products are sold pursuant to SemiQ's terms and conditions of sale in place at the time of order acknowledgement.

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